

FIG. 1a

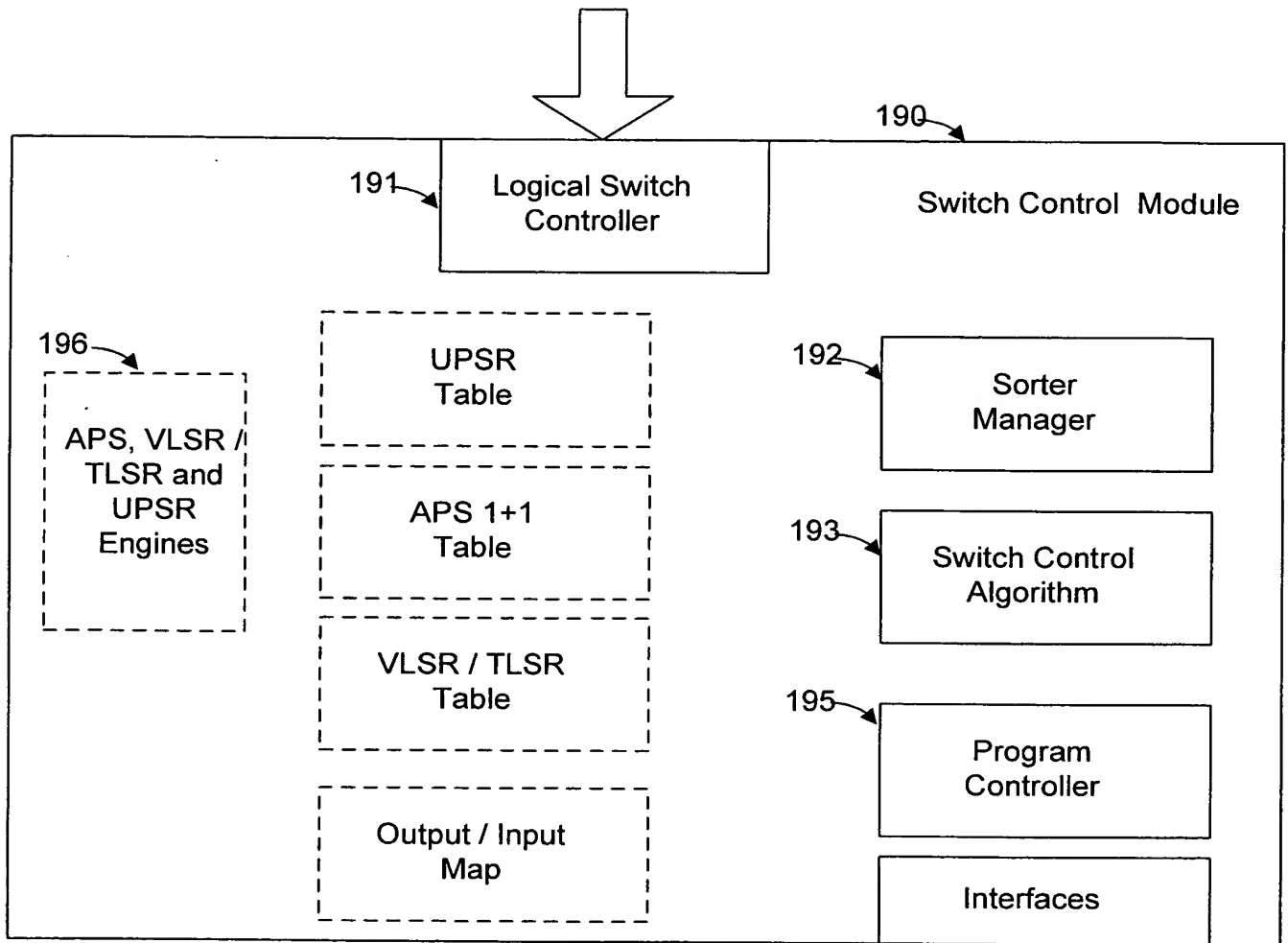


Figure 1b

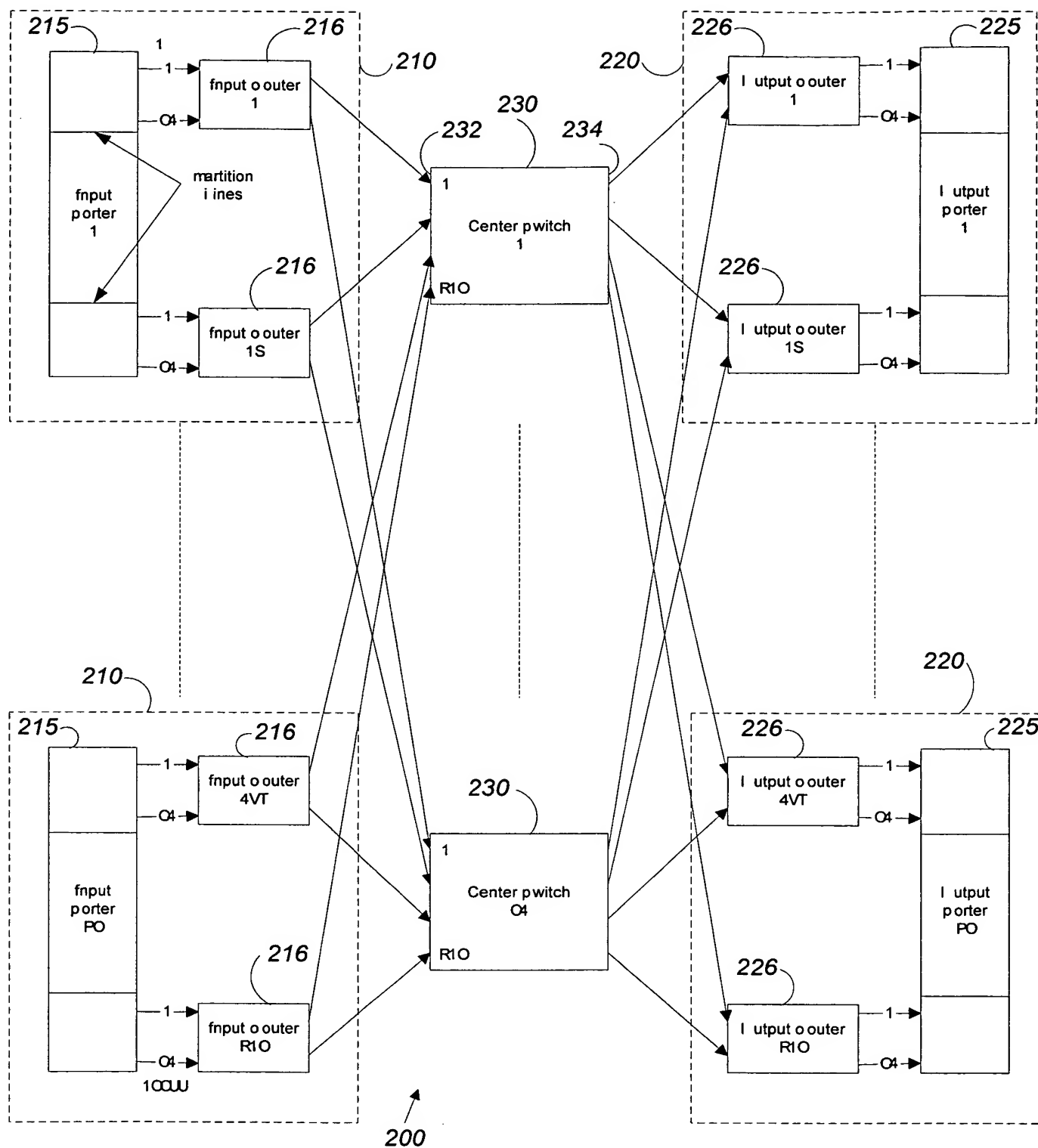


FIG. 2

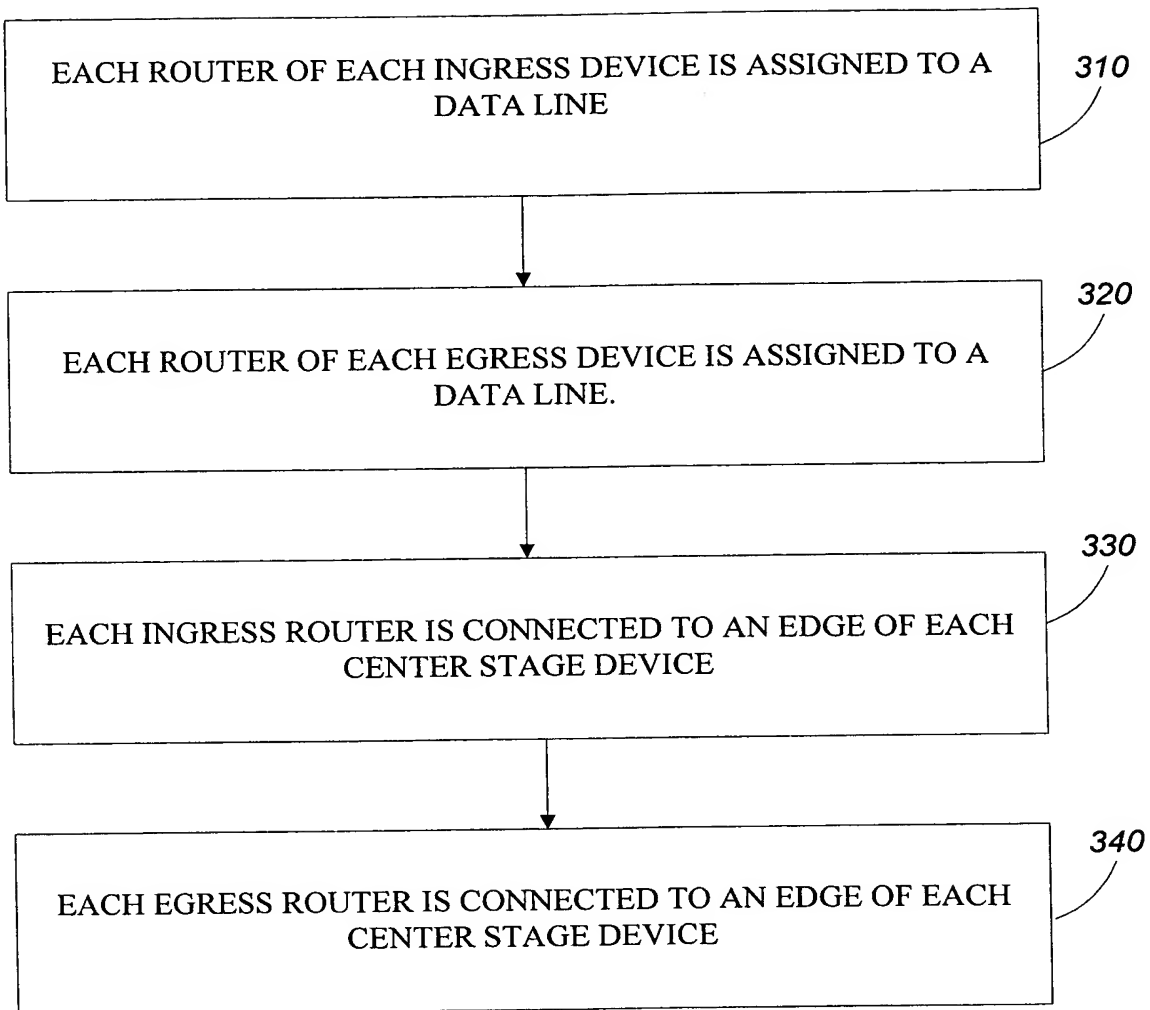


FIG. 3a

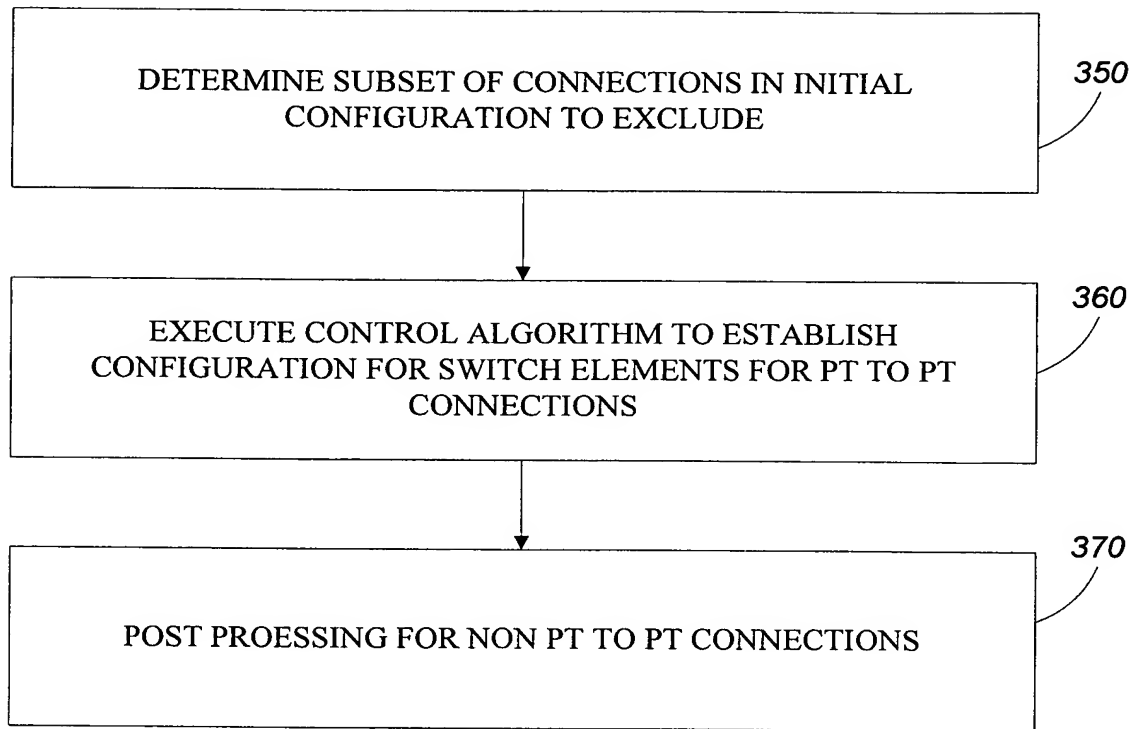


FIG. 3b

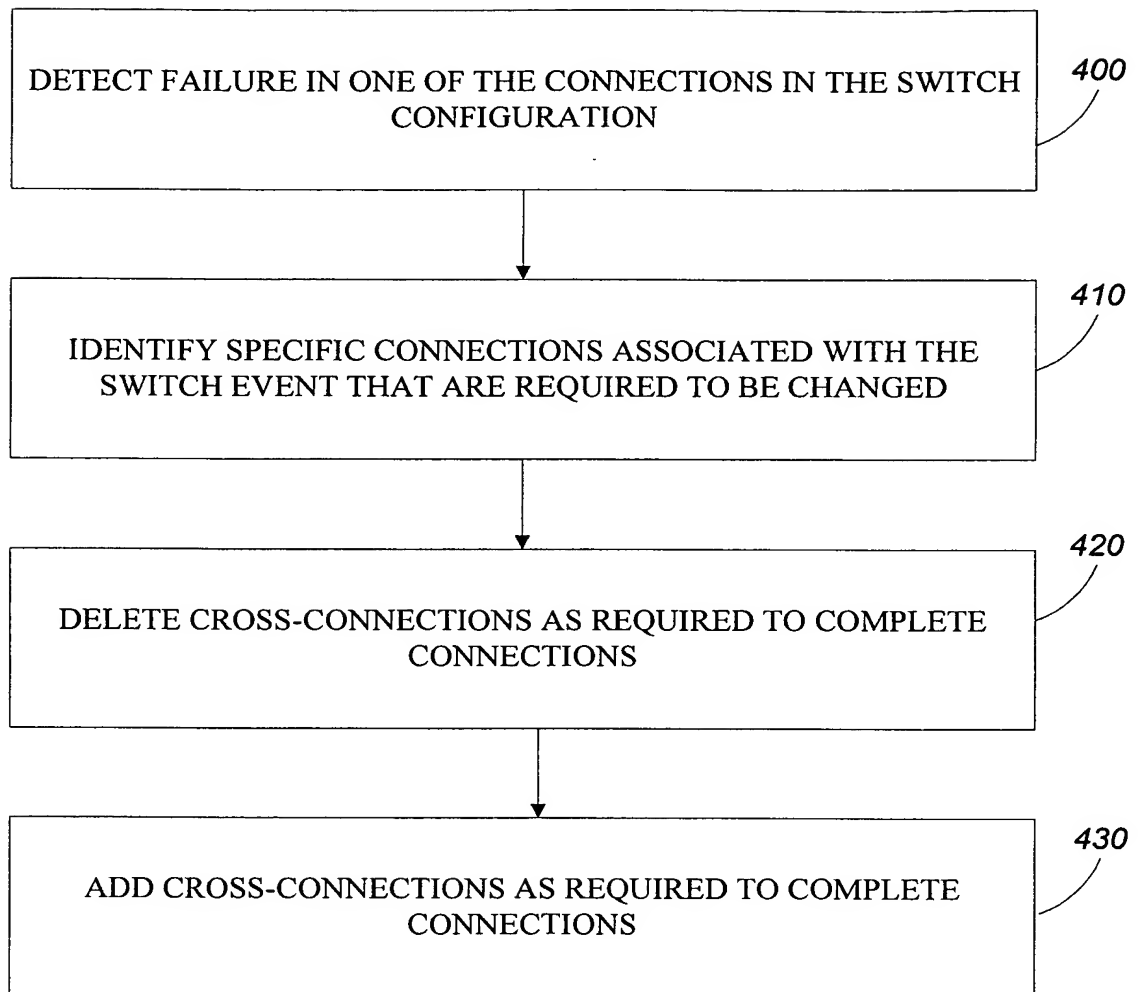


FIG. 4

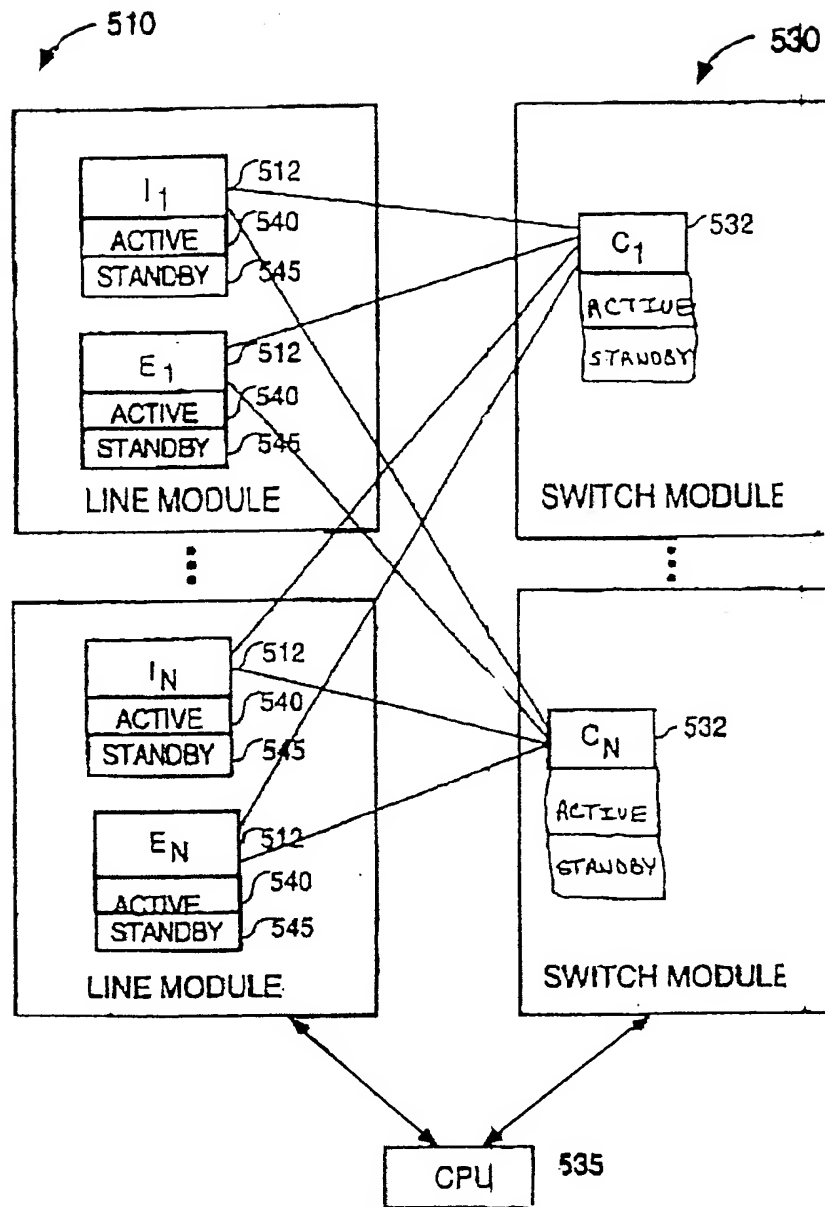


FIG. 5

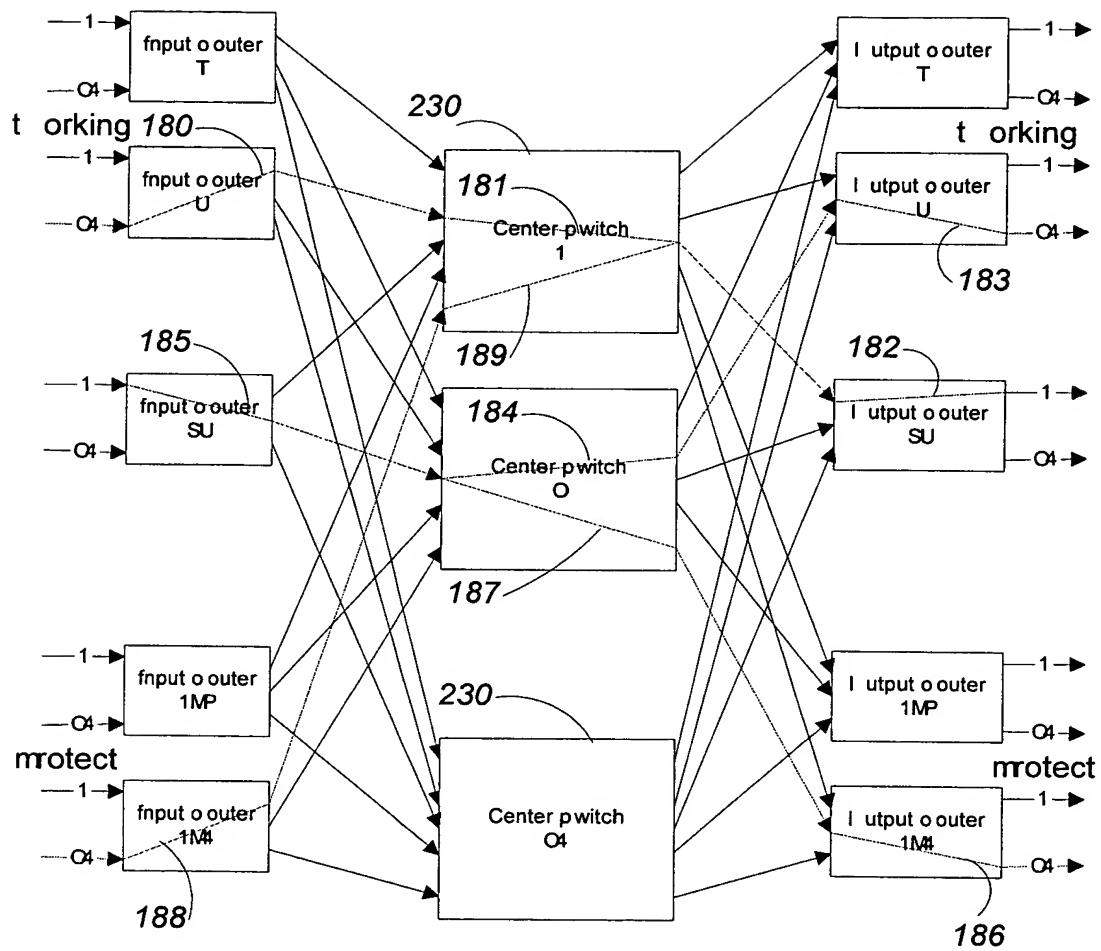


FIG. 6a

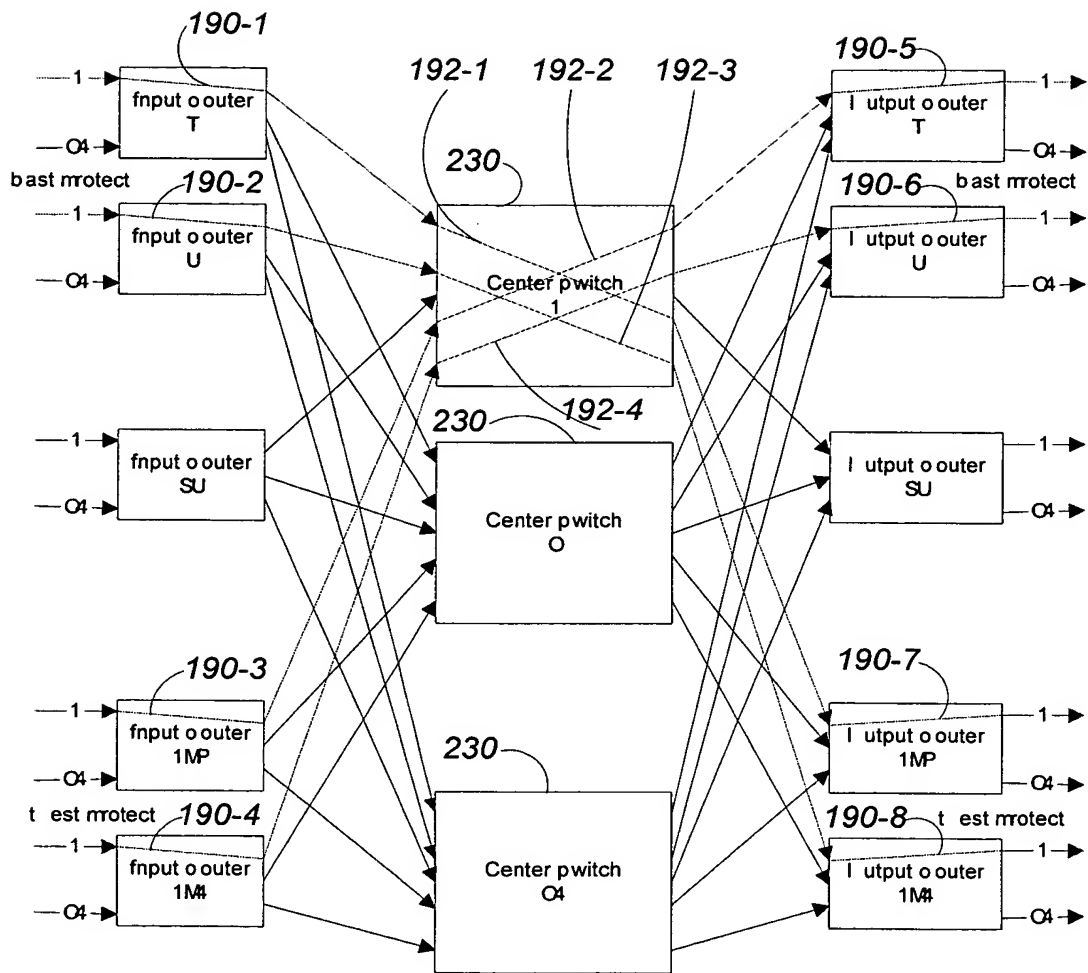


FIG. 6b

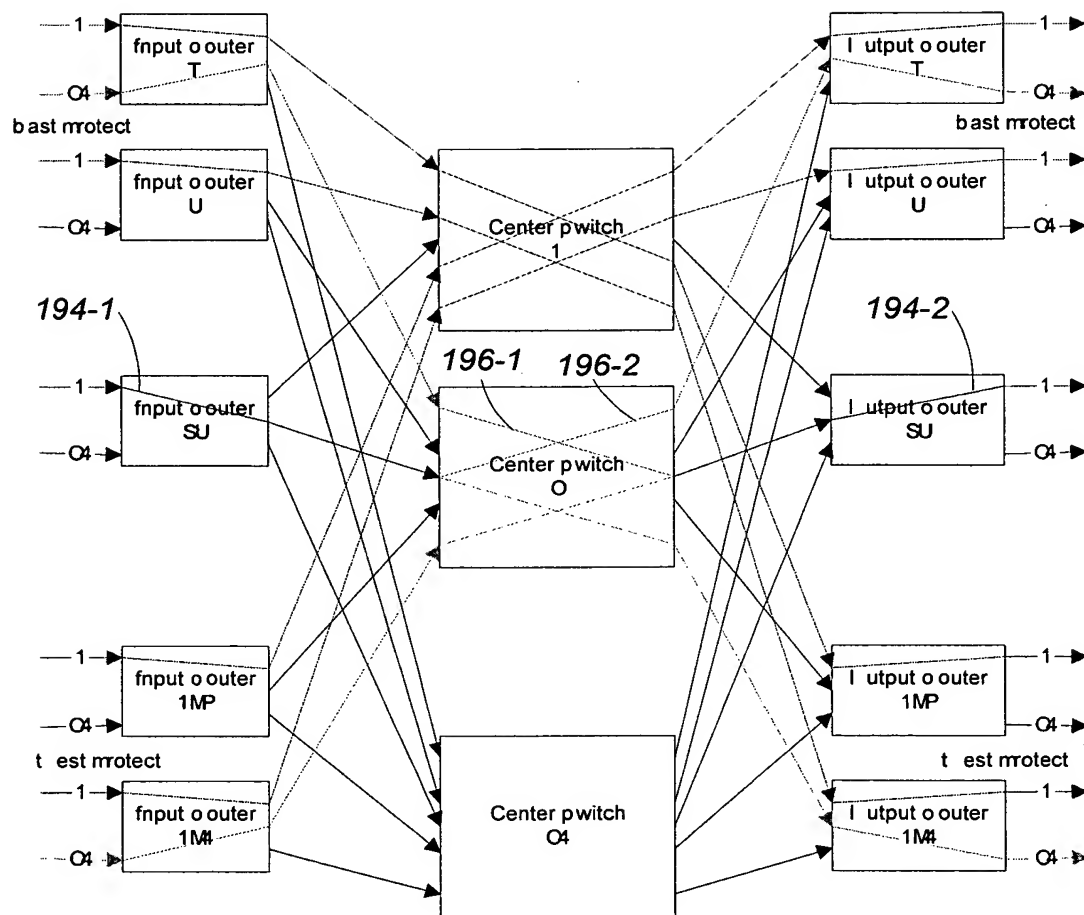


FIG. 6c

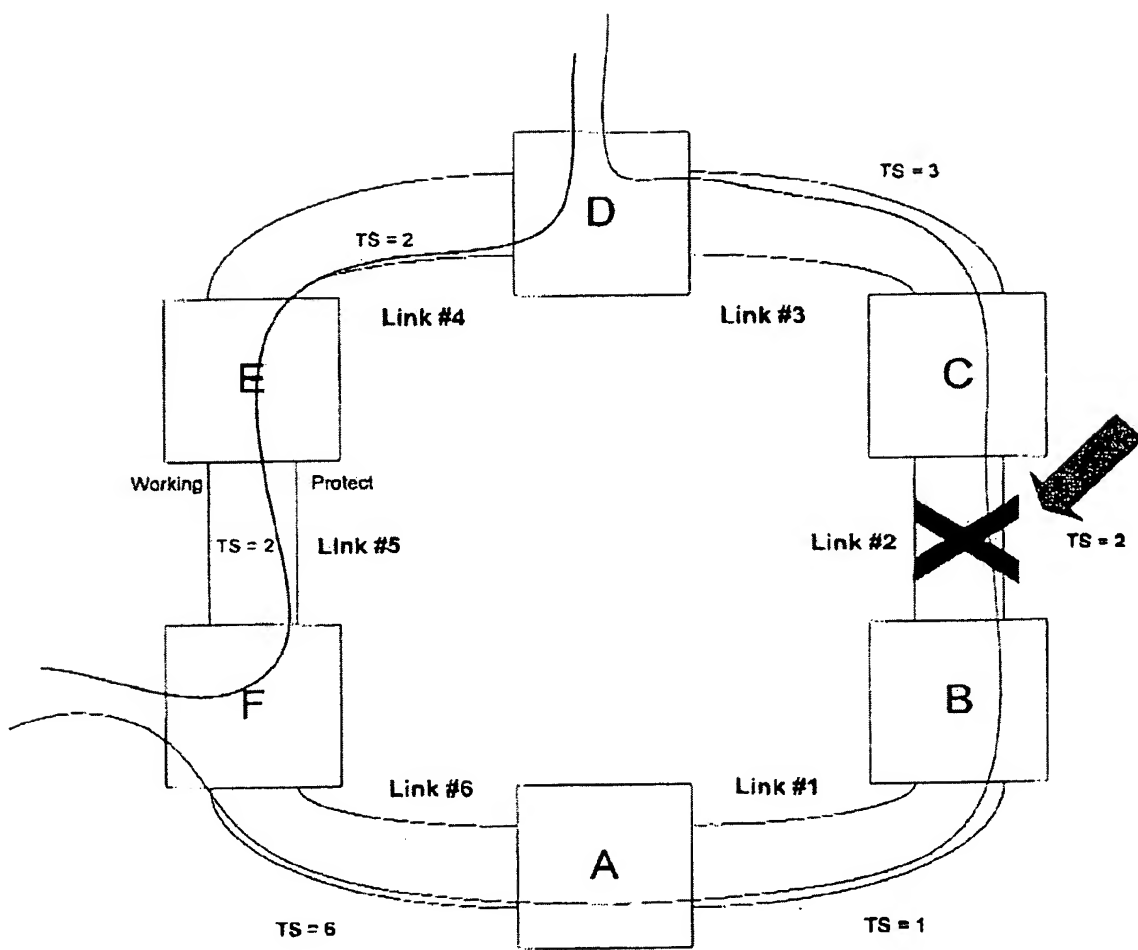


FIG. 6d

Working Timeslot is Dropped for Failure at TLSR Link =														Flattened Drop TimeSlot(ISITS)	
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	
0															
1															
2															
147455															

1 bit Each

18 bits

1 bit Each

18 bits

FIG. 6e

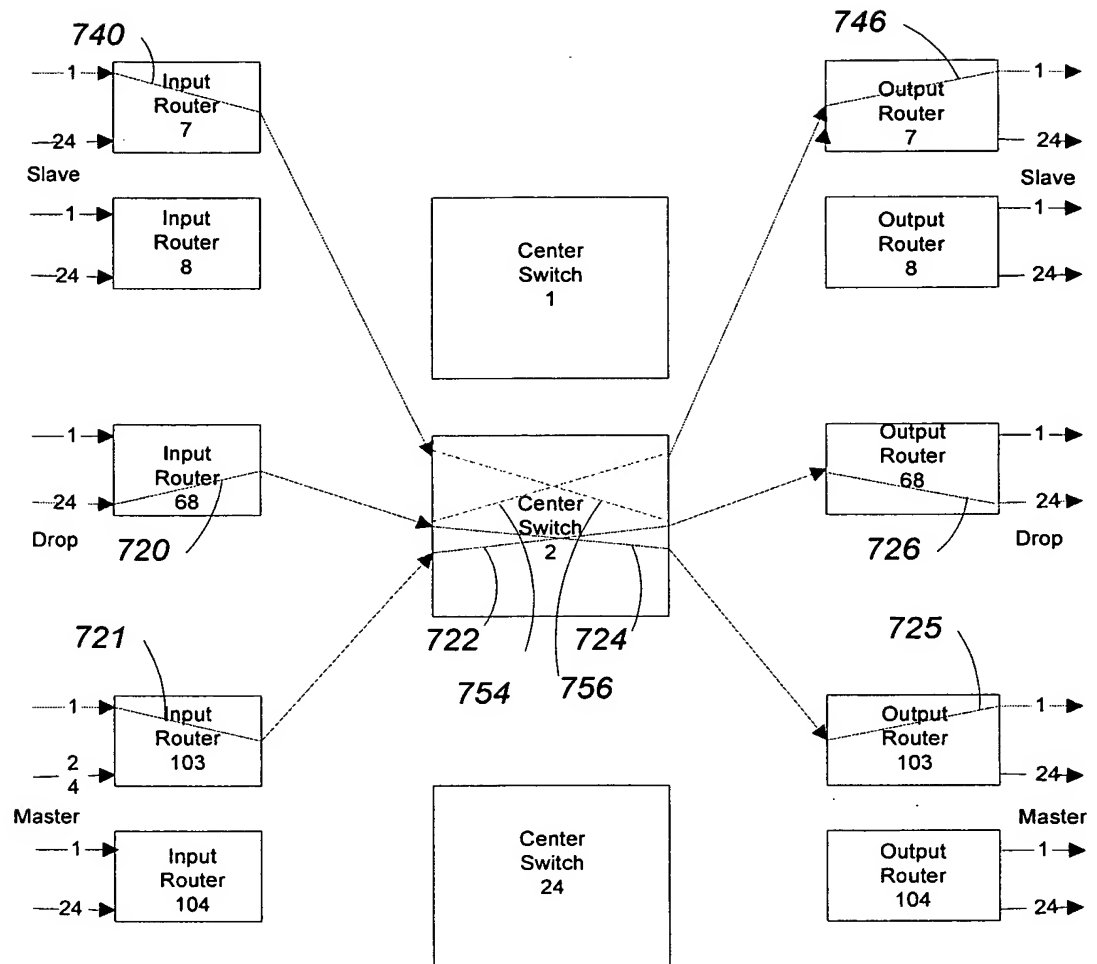


FIG. 7a

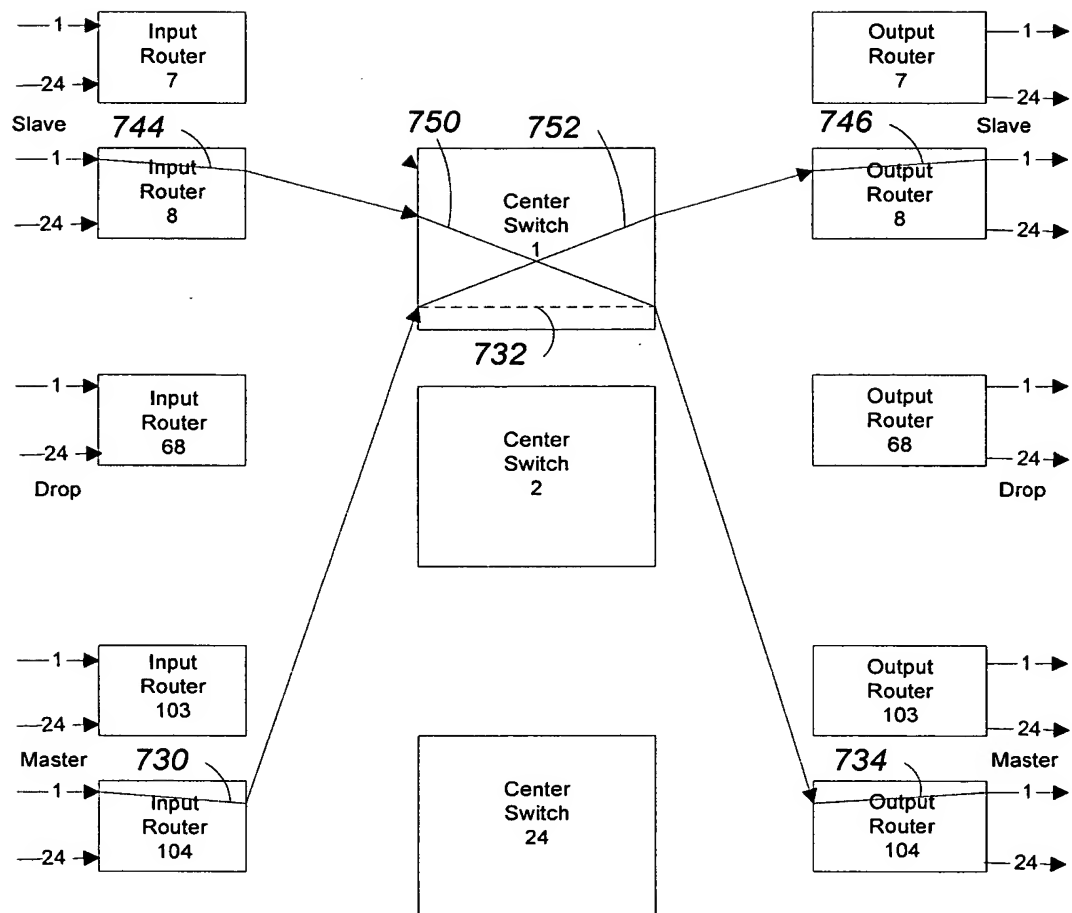


FIG. 7b

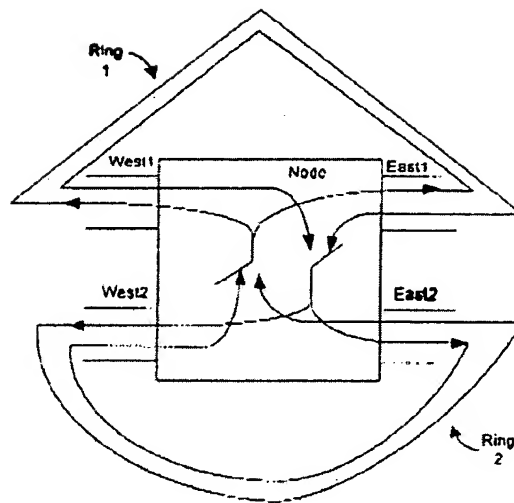


FIG. 7c